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(Rev. 2-32)		Bailey 6-11	10175937 Not yet assigned
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 JL DC	"A 1.92GHz Image-Reject Front-End with Automatic Tuning in a 0.15µm CMOS Technology" , by Mustafa H. Koroglu et al., September 2003, 2003 IEEE International Solid-State Circuits Conference, Paper 15.2, 10 pages.
	"Integrated Continuous-Time Filters" Principles, Design, and Applications, by Y.P. Tsividis and J.O. Voerman, Reprint Volume IEEE Circuits and Systems Society, New York, 1993, pp. 282-2K3.

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